

WHERE PASSION LEADS TO EXCELLENCE



ELSYS
EASTERN EUROPE

MEMBER OF  **ADVANS
GROUP**

Based in Belgrade, ELSYS Eastern Europe is a subsidiary of ADVANS Group which is a European group of experts in electronic systems and software. Our 1000 engineers serve a range of industries: in multimedia/wireless, service, medical, network/telecom, energy, aerospace, defense, automotive and IT.

For our offices in **Belgrade** and **Novi Sad**, Serbia, we are looking for:

Junior Digital/Mixed-Signal Design Verification Engineer 8 positions - Internship and permanent job positions

Following good practice from the past years, ELSYS Eastern Europe is announcing open positions for the summer 2017 internships and entry level permanent positions.

Description:

You will be a part of a digital/mixed-signal design verification team, constituted of verification engineers with different level of experience, from verification experts to verification juniors. In order to achieve IC implementation milestones you will be involved in verification methodology establishment, verification plan creation, development of verification environment and close interaction with other IC implementation teams. Your work on these activities will be challenging and you will have the opportunity to work with industry experts on the state-of-the-art processes, tools and flows.

- Selected candidates will be engaged in development of a digital/mixed-signal verification environment including:
 - architecture definition
 - verification environment implementation in SystemVerilog
 - verification planning
 - full verification in accordance with UVM methodology (SystemVerilog)
- Selected candidates will receive full training and support from our experienced engineers
- Project can be subject of a bachelor/master thesis or seminar paper
- Internship positions details
 - Full time 40h a week during 3 months, starting summer 2017 or differently defined with candidate with ability to attend university obligations and to have summer vacation during this period
 - Monthly compensation and accommodation provided if they are outside of Belgrade/Novi Sad

Qualification Requirements:

- Final year student or fresh graduate with B.Sc. or M.Sc. degree in electrical engineering
- Motivated, proactive, hard working
- Relevant courses/knowledge: basics of digital electronics, programming, FPGA basics, microcontroller architecture
- Skills: VHDL or Verilog, C programming language, object-oriented programming is a plus
- Good knowledge of English language is a plus

Employee Benefits:

- Integration Program in a Professional, Young & Dynamic Team
- Professional Development Opportunities
- Competitive Salaries & Benefits
- Compensation package includes also Additional Health Insurance, Sport & Social activities
- International Work Environment & Traveling Opportunities

All interested candidates are invited to send CV and motivation letter in English, indicating the field of interest, relevant college courses and projects, on the following email: jobs@elsys-eastern.com. Deadline for application is **May 26th**. Interviews are planned for the middle/end of May and the final list of selected candidates will be announced by June 2nd.

www.elsys-eastern.com